

PATENT

AMENDMENTS TO THE CLAIMS

Please amend the claims as indicated in the following listing of all claims:

1. (Currently Amended) A method of detecting user activity of a computer, the method comprising:  
taking a temperature reading in connection with the computer;  
determining a desired operating temperature for the computer;  
comparing the temperature reading and the desired operating temperature to determine a temperature difference of the temperature reading from the desired operating temperature; and  
determining a user activity indication based on the temperature difference.
2. – 3. (Cancelled)
4. (Currently Amended) The method of claim 1, further comprising adjusting a clock signal frequency applied to a processor within the computer as a function of the temperature difference of the temperature reading from the desired operating temperature.
5. (Original) The method of claim 4, wherein the clock signal frequency is increased when the temperature difference is positive.
6. (Original) The method of claim 4, wherein the clock signal frequency is decreased when the temperature difference is negative.
7. (Original) The method of claim 4, further comprising adjusting a voltage applied to the processor.
8. (Original) The method of claim 7, wherein the voltage is increased when the temperature difference is positive.
9. (Original) The method of claim 7, wherein the voltage is decreased when the temperature difference is negative.

## PATENT

10. – 13. (Cancelled)

14. (Currently Amended) A method for use with a computer having a processor, the method comprising:

taking a temperature reading in connection with the computer;  
determining a desired operating temperature for the computer;  
comparing the temperature reading and the desired operating temperature to determine a temperature difference; and  
increasing a performance state of the processor when the temperature difference of the temperature reading from the desired operating temperature is positive.

15. (Currently Amended) A system comprising:

a processor device having a clock input to receive a clock signal;  
a temperature measurement device responsive to the processor device, the temperature measurement device configured to take a temperature measurement for the processor; and  
control logic responsive to the temperature measurement device, the control logic to compare the temperature measurement to a desired operating temperature to determine a temperature difference of the temperature reading from the desired operating temperature; and

wherein the clock signal applied to the clock input has a frequency that is increased when the temperature difference is positive.

16. (Original) The system of claim 15, wherein the processor device further comprises a voltage input and wherein the voltage applied to the voltage input is increased when the temperature difference is positive.

17. (Original) The system of claim 15, wherein the frequency of the clock signal is decreased when the temperature difference is negative.

18. (Original) The system of claim 16, wherein the voltage applied to the voltage input is decreased when the temperature difference is negative.

## PATENT

19. (Original) The system of claim 16, further comprising a lookup table containing a plurality of desired operating temperatures for the processor, each of the desired operating temperatures based on different operating characteristics associated with the processor.

20. (Original) The system of claim 19, wherein the operating characteristics include clock frequency and voltage applied to the processor.

21. (Currently Amended) A computing system comprising:  
a processor device having a clock input to receive a clock signal;  
an operating system that allows a user to select between a manual clock speed mode of operation and an automatic temperature-based mode of operation;  
a temperature measurement device responsive to the processor device, the temperature measurement device configured to take a temperature measurement for the processor device;  
control logic responsive to the temperature measurement device, the control logic to compare the temperature measurement with a desired operating temperature to determine a temperature difference; and  
wherein when the computing system is in the manual clock speed mode of operation, the clock signal applied to the clock input has a substantially constant frequency and when the computing system is in the automatic temperature-based mode of operation, the clock signal applied to the clock input has a frequency that is adjusted based on the temperature difference of the temperature reading from the desired operating temperature.

22. (Original) The computer system of claim 21, wherein the temperature measurement device takes a plurality of temperature measures and the temperature measurement is determined by taking an average of the plurality of temperature measurements.

23. (Original) The computer system of claim 21, wherein the predetermined desired operating temperature is read from a look-up table.

## PATENT

24. (Original) The computer system of claim 21, wherein the desired operating temperature is computed based on a plurality of factors, the plurality of factors including the current frequency, voltage, and temperature of the processor device.

25. (Original) The computer system of claim 24, wherein the desired operating point is modified based on environmental variations affecting the processor device.

26. (New) A method comprising:  
determining user activity for one or more processors, wherein the determining is based, at least in part, on a measured temperature that corresponds to the processors; and  
adjusting performance of the one or more processors in accordance with the determined user activity.

27. (New) The method of claim 26, wherein the determining comprises comparing the measured temperature against a desired operation temperature.

28. (New) The method of claim 27, wherein the adjusting comprises adjusting performance as a function of a difference of the measured temperature the from the desired operation temperature.

28. (New) The method of claim 27, wherein the user activity has increased or is high if the measured temperature exceeds the desired operation temperature, and the user activity has decreased or is low if the measured temperature is less than desired.

29. (New) The method of claim 26, wherein the adjusting comprises adjusting one or more of clock frequency and operating voltage.

30. (New) The method of claim 26, wherein the adjusting performance comprises increasing performance of the at least one of the processors if the determined user activity increases or is high.

PATENT

31. (New) The method of claim 26, wherein the adjusting comprises decreasing performance of at least one of the processors if the determined user activity decreases or is low.

32. (New) The method of claim 26, wherein the measured temperature is a measured temperature of at least one of the one or more processors or a system that includes the one or more processors.

33. (New) An apparatus comprising:  
a set of one or more processors;  
a temperature sensor operable to measure temperature corresponding to the set of processors;  
a store unit operable to store desired operating temperatures; and  
means for increasing performance of the set of processors if a temperature measured by the temperature sensor exceeds a desired operating temperature stored in the store unit.

34. (New) The apparatus of claim 33 further comprising means for periodically activating the temperature sensor to measure temperature.